L Number	Hits	Search Text	DB	Time stamp
1	0	ICE and debug$3 and microcontroller and nguyen-ba.xp.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/13 15:14
2	24	717/124-135	USPAT; US-PGPUB	2004/08/13 15:14
3	1566	(717/124-135).CCLS.	USPAT; US-PGPUB	2004/08/13 15:15
4	0	ICE and debug$3 and nguyen-ba.xa.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/13 15:15
5	528	ICE and debug$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/13 15:15
6	2	(ICE and debug$3) and nguyen-ba.xp.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/13 15:17
7	0	(in adj circuit adj emulation) and debug$3 and nguyen-ba.xp.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/13 15:18
8	0	(in adj circuit adj emulation) and debug$3 and nguyen-ba.xa.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/13 15:19
9	0	(in adj circuit adj emulation) and nguyen-ba.xa.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/13 15:19
10	20	interface and debug$3 and nguyen-ba.xa.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/13 15:22
11	64	jtag and nguyen-ba.xa. or nguyen-ba.xp.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/13 15:29
12	19	(jtag and nguyen-ba.xa. or nguyen-ba.xp.) and debug$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/13 15:25
13	45	(jtag and nguyen-ba.xa. or nguyen-ba.xp.) not ((jtag and nguyen-ba.xa. or nguyen-ba.xp.) and debug$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/13 15:25
14	0	jtag and (nguyen-ba.xa. or nguyen-ba.xp.)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/13 15:29
15	3137	jtag	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/13 15:29

16	0	jtag and nguyen-ba.xa.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/13 15:29
17	0	jtag and nguyen-ba.xp.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/13 15:30
18	1001	jtag and debug$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/13 15:30
19	0	(jtag and debug$3) and 717/124-135	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/13 15:31
21	0	((jtag and debug$3) and 717/124-135.ccls.) and nguyen-ba.xa.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/13 15:31
22	0	((jtag and debug$3) and 717/124-135.ccls.) and nguyen-ba.xp.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/13 15:31
23	0	((jtag and debug$3) and 717/124-135.ccls.) and "nguyen ba".xp.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/13 15:31
20	57	(jtag and debug$3) and 717/124-135.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/13 15:33
24	1	ICE with microcontroll$3 with halt$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/13 15:34
25	0	emulat$3 with microcontroll$3 with halt$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/13 15:35
26	3	debug$3 with microcontroll$3 with halt$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/13 15:38
27	0	ICE and debug$3 near (microcontroller adj code)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/13 15:39
28	5	debug$3 near (microcontroller adj code)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/13 15:40
29	101	microcontroller adj code	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/13 15:40

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30	29	(microcontroller adj code) and debug$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/13 15:41
31	21423033	((microcontroller adj code) and debug$3) @ad<20011115	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/13 15:41
32	21	((microcontroller adj code) and debug$3) and @ad<20011115	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/13 15:45
33	1992	halt$3 near state	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/13 15:45
34	3	(halt$3 near state) and (in adj circuit adj emulation)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/13 15:54
35	8	virtual adj microcontroller	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/13 16:25
36	1425	(703/23-28).CCLS.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/13 16:26
37	989	(714/30-31,34).CCLS.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/13 16:27
38	2389	((703/23-28).CCLS.) ((714/30-31,34).CCLS.)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/13 16:27
39	2181	(((703/23-28).CCLS.) ((714/30-31,34).CCLS.)) and @ad<20011115	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/13 16:27
40	2	emulat$3 with debug$3 with (microcontroller adj code)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/13 16:30
41	6	debug$3 with (microcontroller adj code)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/13 16:30
42	2	(emulat$3 with debug$3 with (microcontroller adj code)) not "42"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/13 16:31
43	4	(debug$3 with (microcontroller adj code)) not (emulat$3 with debug$3 with (microcontroller adj code))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/08/13 16:32

44	1	(((((703/23-28).CCLS.) ((714/30-31,34).CCLS.)) and @ad<20011115) and (production adj microcontroller)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/13 16:36
45	0	(((((703/23-28).CCLS.) ((714/30-31,34).CCLS.)) and @ad<20011115) and (FPGA near2 ICE)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/13 16:37
46	1	(((((703/23-28).CCLS.) ((714/30-31,34).CCLS.)) and @ad<20011115) and (debug$3 near (high adj frequency))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/13 16:40
47	0	(((((703/23-28).CCLS.) ((714/30-31,34).CCLS.)) and @ad<20011115) and (debug$3 adj (high adj frequency))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/13 16:40
48	60	(((((703/23-28).CCLS.) ((714/30-31,34).CCLS.)) and @ad<20011115) and (debug near interfac$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/13 16:41
49	11	(((((703/23-28).CCLS.) ((714/30-31,34).CCLS.)) and @ad<20011115) and ((debug near interfac$3) with communicat$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/13 16:43

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